

**AMENDMENTS TO THE CLAIMS**

This listing of claims will replace all prior versions of claims in the application:

**Listing of Claims:**

1. (Currently Amended) A performance monitoring system, comprising:  
a performance component that initiates at least one low-priority thread involving at least one computing resource, the low-priority thread comprising at least one selected from the group consisting of a memory-intensive operation thread and a computationally-intensive operation thread; and  
a monitoring component that obtains at least one performance parameter for the computing resource derived, at least in part, from the low-priority thread initiated by the performance component.
2. (Canceled)
3. (Original) The system of claim 1, the computing resource comprising at least one selected from the group consisting of a central processing unit (CPU) and a memory resource.
4. (Original) The system of claim 1, the performance parameter comprising at least one from the group consisting of available central processing unit (CPU) processing time, available memory, and available CPUs.
5. (Original) A dynamically linked library (DLL) comprising the system of claim 1.

6. (Currently Amended) A method for facilitating computing system performance, comprising:

executing at least one low-priority thread involving at least one computing resource, the low-priority thread comprising at least one selected from the group consisting of a memory-intensive operation thread and a computationally-intensive operation thread; and

obtaining at least one performance parameter for the computing resource derived, at least in part, from execution of the low-priority thread.

7. (Canceled)

8. (Original) An entity employing the method of claim 6 comprising at least one selected from the group consisting of a light-weight operating system, a self-tuning application, a cell phone, a personal digital assistant (PDA), a logical central processing unit (CPU), an application programming interface (API), a computer, a server, and a handheld electronic device.

9. (Original) A computer readable medium having stored thereon computer executable components of the system of claim 1.

10. (Original) A device employing the system of claim 1 comprising at least one selected from the group consisting of a computer, a server, and a handheld electronic device.

11. (Currently Amended) A performance monitoring system, comprising:

a performance component that initiates at least one high-frequency interrupt involving at least one computing resource, the high-frequency interrupt comprising an interrupt with a frequency of at least approximately 300 Hertz; and

a monitoring component that obtains at least one performance parameter for the computing resource derived, at least in part, from the high-frequency interrupt initiated by the performance component.

12. (Canceled)

13. (Original) The system of claim 11, the computing resource comprising at least one selected from the group consisting of a central processing unit (CPU) and a memory resource.
14. (Original) The system of claim 11, the performance parameter comprising at least one from the group consisting of available central processing unit (CPU) processing time, available memory, and available CPUs.
15. (Original) A dynamically linked library (DLL) comprising the system of claim 11.
16. (Currently Amended) A method for facilitating computing system performance, comprising:  
    executing at least one high-frequency interrupt involving at least one computing resource,  
    the high-frequency interrupt comprising an interrupt with a frequency of at least approximately 300 Hertz; and  
    obtaining at least one performance parameter for the computing resource derived, at least in part, from execution of the high-frequency interrupt.
17. (Canceled)
18. (Original) An entity employing the method of claim 16 comprising at least one selected from the group consisting of a light-weight operating system, a self-tuning application, a cell phone, a personal digital assistant (PDA), a logical central processing unit (CPU), an application programming interface (API), a computer, a server, and a handheld electronic device.
19. (Original) A computer readable medium having stored thereon computer executable components of the system of claim 11.
20. (Original) A device employing the system of claim 11 comprising at least one selected from the group consisting of a computer, a server, and a handheld electronic device.

21. (Currently Amended) A performance monitoring system, comprising:  
a performance component that initiates at least one low-priority thread involving at least one computing resource and at least one high-frequency interrupt involving at least one computing resource, wherein the low-priority thread comprising at least one selected from the group consisting of a memory-intensive operation thread and a computationally-intensive operation thread; and  
a monitoring component that obtains at least one performance parameter for the computing resource derived, at least in part, from at least one selected from the group consisting of the low-priority thread and the high-frequency interrupt initiated by the performance component.
22. (Canceled)
23. (Original) The system of claim 21, the high-frequency interrupt comprising an interrupt with a frequency of at least approximately 300 Hertz.
24. (Original) The system of claim 21, the computing resource comprising at least one selected from the group consisting of a central processing unit (CPU) and a memory resource.
25. (Original) The system of claim 21, the performance parameter comprising at least one from the group consisting of available central processing unit (CPU) processing time, available memory, and available CPUs.

26. (Currently Amended) A method for facilitating computing system performance, comprising:

executing at least one low-priority thread involving at least one computing resource and at least one high-frequency interrupt involving at least one computing resource, wherein the low-priority thread comprising at least one selected from the group consisting of a memory-intensive operation thread and a computationally-intensive operation thread; and

obtaining at least one performance parameter for the computing resource derived, at least in part, from execution of at least one selected from the group consisting of the low-priority thread and the high-frequency interrupt.

27. (Canceled)

28. (Original) An entity employing the method of claim 26 comprising at least one selected from the group consisting of a light-weight operating system, a self-tuning application, a cell phone, a personal digital assistant (PDA), a logical central processing unit (CPU), an application programming interface (API), a computer, a server, and a handheld electronic device.

29. (Original) A computer readable medium having stored thereon computer executable components of the system of claim 21.

30. (Original) A device employing the system of claim 21 comprising at least one selected from the group consisting of a computer, a server, and a handheld electronic device.

31. (Currently Amended) A system that facilitates performance monitoring, comprising:  
means for initiating at least one selected from the group consisting of at least one low-priority thread involving at least one computing resource and at least one high-frequency interrupt in at least one computing resource, wherein the low-priority thread comprising at least one selected from the group consisting of a memory-intensive operation thread and a computationally-intensive operation thread; and  
means for obtaining at least one performance parameter for the computing resource derived, at least in part, from at least one selected from the group consisting of the low-priority thread and the high-frequency interrupt.
32. (Currently Amended) A data packet transmitted between two or more computer components that facilitates computing performance, the data packet comprising, at least in part, information relating to a computing performance system that utilizes, at least in part, at least one selected from the group consisting of at least one low-priority thread with memory and computationally-intensive operations and at least one high-frequency interrupt to determine availability of computing resources, the high-frequency interrupt comprising an interrupt with a frequency of at least approximately 300 Hertz.